

Patent

Attorney Docket No.: Intel 2207/560202



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Amit MERCHANT et al.

SERIAL NO. : 10/690,634

FILED : October 23, 2003

FOR : INTERFACE TO A MEMORY SYSTEM FOR A
PROCESSOR HAVING A REPLAY SYSTEM

GROUP ART UNIT : 2183

EXAMINER : Henry TSAI

ASSIGNEE : INTEL CORPORATION

M/S: AMENDMENT
Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING

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Barbara Vance
Dated: December 5, 2005 Signature Barbara Vance

AMENDMENT

Dear Sir:

The following amendments are presented in response to the Office Action dated June 3, 2005.

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Claims are reflected in the listing of claims which begin on page 4 of this paper.

Application No.: 10/690,634

Amendment dated: December 5, 2005

Reply to Office Action dated: June 3, 2005

Amendments to the Drawings begin on page 12 of this paper.

Remarks/Arguments begin on page 13 of this paper.

An **Appendix** including annotated figures and replacement figures is attached following page 14 of this paper.